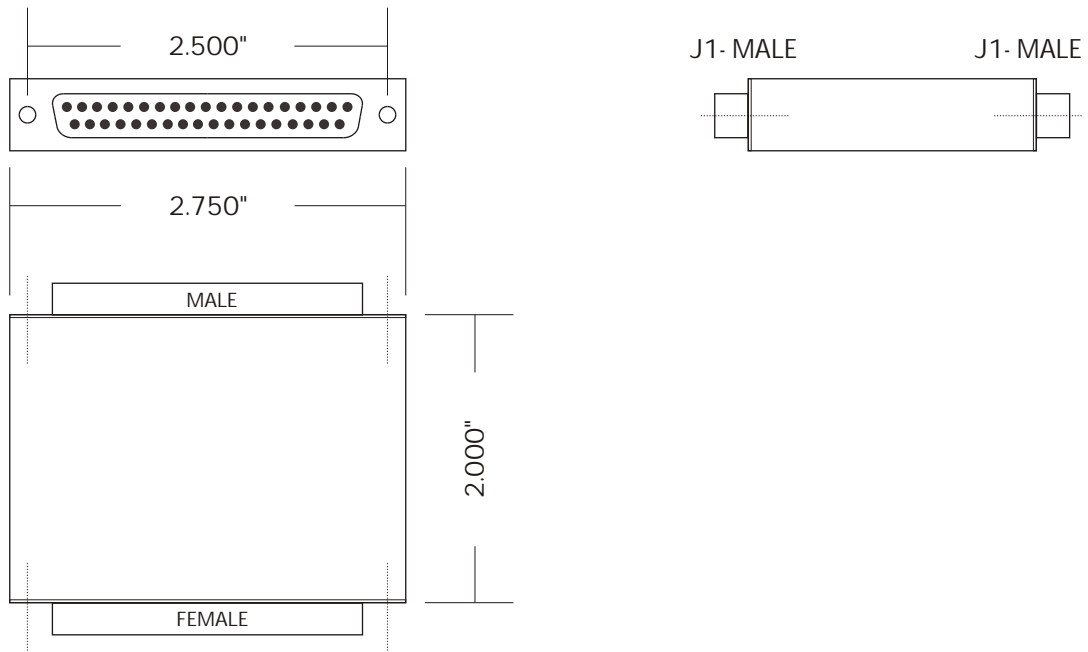


REV	DATE



OUTLINE DRAWING

RISE TIME: 520 TO 620 ns
FROM 0.8 V TO 2.8 V

FALL TIME: 280 TO 340 ns
FROM 2.8 V TO 0.8 V

SOURCE/LOAD IMPEDANCE: TTL LOGIC IMPED.

LINE VOLTAGE: 50 V RMS'

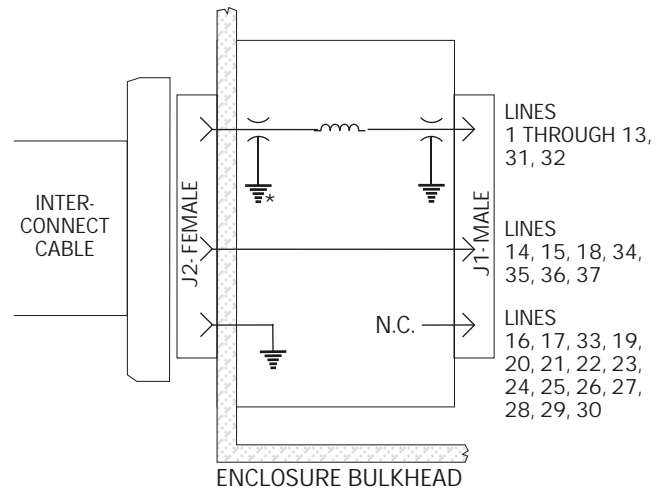
DATA RATES: DC TO 750 Kbps

INSERTION LOSS: 0.5 dB MAX.

ATTENUATION	
Frequency	Typical
100 MHz	0.5 dB
500 MHz	5 dB
1 MHz	20 dB
10 MHz	45 dB
50 MHz	45 dB

*IN 50 OHM SYSTEM

SPECIFICATIONS



*CHASSIS GROUND

FUNCTIONAL DIAGRAM